



Clock&osc

Full name	Status	Vendor	Geometry	Variation	Feature
Low jitter PLL	●	HLMC	40nm	LP	1. Both 2.5V and 1.1V power supplies are required 2. Low RMS jitter <3ps
Digital PLL	●				1.Single 1.1V power supply is required 2. Broad input frequency range is from 200KHz to 150MHz
Low jitter PLL	●		55nm		1. Both 2.5V and 1.2V power supplies required 2.Low RMS jitter about 3ps
Digital PLL	●				1.Single 1.2V power supply required 2. Broad input frequency range is 200KHz to 260MHz
Oscillator	●	TSMC	55nm	LP	1. Single power supply required. 2. Output frequency is 27MHz. 3. Low power < 200uA
PLL	●	XMC	55nm	LP 1P5M	1. Single 1.2V power supply required 2. Low RMS jitter: 8ps @ 1200 MHz 3. Die Size: 0.02466mm ²
PLL	●	TPSCo	110nm	CIS	1. Required voltage of 1.2/3.3v 2. Low RMS period jitter 8ps
All digital PLL	●	SMIC	130nm	Generic	1. Single 1.2V power supply required 2. Broad input frequency range is 2MHz to 250MHz

● Production ○ Silicon Proven