

High Speed Interface

Full name	Status	Vendor	Geometry	Variation	Feature
USB 2.0 PHY	●	HLMC	55nm	LP	<ul style="list-style-type: none"> 1. Supports Low level Protocol(SYNC/ EOP generation and detection) 2. Bandgap Reference Bias generation
LVDS	●				<ul style="list-style-type: none"> 1. Configurable Dual Port LVDS Transmitter PHY IP 2. Support Both JEIDA and VESA Bit Mapping for LVDS Signaling
HDMI RX	●				<ul style="list-style-type: none"> 1. Single link HDMI receiver with PHY and Controller 2. Fully compliant with HDMI 1.4b specification and DVI 1.0 specification 3. Support 3D formats, 4Kx2K resolution and other common formats
DDR2/3 Combo PHY	●				<ul style="list-style-type: none"> 1. Support data rate from 400Mbps to 667Mbps for DDR2, from 800Mbps to 1066Mbps for DDR3 2. Interface to any memory controller (MC) through the DDR PHY
HDMI TX	●	SMIC	40nm	LL 1P8M	<ul style="list-style-type: none"> 1. Single link HDMI source transmitter with PHY and Controller 2. Compliant with HDMI 1.4b specification and DVI 1.0 specification
Type-C DRP Controller	●	HHG	180nm	BCD	<ul style="list-style-type: none"> 1. Compliant with standard A/standard B/micro B/mini B; D+/D- pin can meet the handshaking detections of different portable devices 2. Support 5V/9V/12V/15V/20V, the maximum current is 3A
LVDS	○	TSMC	55nm	G	<ul style="list-style-type: none"> 1. Operation Voltage Range: 3.3v for output driver with 1.1v for core function block 2. Data rate 25 to 150Mbps (@CLK=25 ~ 150MHz)
Multi-Gigabits Serdes	●	TSMC	40nm	G	<ul style="list-style-type: none"> 1.4 Channel per Quad, X4~X12 Quads, 300Mbps~10.3125Gbps 2.Low Power Consumption (PCS+PMA+I5)
LVDS	○	TSMC	40nm	G	<ul style="list-style-type: none"> 1. Operation Voltage Range: 3.3v for output driver and 1.1v for core function block 2. Data rate 25 to 150Mbps (@CLK=25 ~ 150MHz)

Full name	Status	Vendor	Geometry	Variation	Feature
Multi-Gigabits Serdes	●	TSMC	28nm	HPC+	<ul style="list-style-type: none"> 1. Four Channel per Quad, 500Mbps ~13.1Gbps 2. Low Power Consumption (PCS+PMA)
USB 2.0 PHY with UTMI	○	TSMC	130nm	G	<ul style="list-style-type: none"> 1.Supports USB High Speed (480Mbps) and Full Speed (12Mbps) operation modes 2.Supports USB High Speed cable connect/disconnect detection

● Production ○ Silicon Proven